	Application No.	Applicant(s)	
Notice of Allowability			
	10/823,485 Examiner	JEON ET AL. Art Unit	
, and the second	Lammer	Artonic	
	Stephen W. Smoot	2813	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in to the community or other appropriate community of the community of	his application. If not included ication will be mailed in due cours	se. THIS
1. X This communication is responsive to applicant's amendment	ent filed on 18 January 2005.		
2. The allowed claim(s) is/are <u>1,4,6-10 and 12</u> .			
3. $igotimes$ The drawings filed on <u>12 April 2004</u> are accepted by the E	xaminer.		
4. Acknowledgment is made of a claim for foreign priority u a) All b) Some* c) None of the: 1. Certified copies of the priority documents hav 2. Certified copies of the priority documents hav 3. Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 5. A SUBSTITUTE OATH OR DECLARATION must be subn INFORMAL PATENT APPLICATION (PTO-152) which giv 6. CORRECTED DRAWINGS (as "replacement sheets") mu (a) including changes required by the Notice of Draftsper 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in 7. DEPOSIT OF and/or INFORMATION about the depondant of the proper in the p	e been received. e been received in Application ocuments have been received of this communication to file a MENT of this application. Initted. Note the attached EXAN res reason(s) why the oath or one at the submitted. From Seat Drawing Review of Seat Amendment / Comment or in the the header according to 37 CFR posit of BIOLOGICAL MATEL	No. 09/948,425 In this national stage application to this national stage application to reply complying with the require MINER'S AMENDMENT or NOTIC leclaration is deficient. (PTO-948) attached the Office action of drawings in the front (not the back 1.121(d). RIAL must be submitted. Note	ments CE OF
 Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/Paper No./Mail Date	6. ☐ Interview Sur Paper No./M (08), 7. ☐ Examiner's A 8. ☑ Examiner's S 9. ☐ Other	rmal Patent Application (PTO-15 nmary (PTO-413), lail Date mendment/Comment tatement of Reasons for Allowan W. Smoot Xaminer / AU 2	ce

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DETAILED ACTION

This Office action is in response to applicant's amendment filed on 18 January 2005.

Response to Arguments

1. Applicant's arguments, see pages 6-7, filed 18 January 2005, with respect to the previously applied IBM Technical Disclosure have been fully considered and are persuasive. It is agreed that this reference lacks the feature of either "the conductive patterns are spaced apart from the contact window" or "the plurality of conductive patterns is spaced apart from the contact window", as is currently claimed by the applicant in all pending claims. Accordingly, the rejection of claims 1, 4, 6-10, based on the IBM Technical Disclosure Bulletin has been withdrawn. Also, the 102(b) rejection of claims 1, 10, 12 based on Sakaemori (JP 10-270555 A) has been withdrawn because, as pointed out by the applicant, independent claims 1, 10 have been amended to include features that are not anticipated by Sakaemori.

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Allowable Subject Matter

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2. Claims 1, 4, 6-10, 12 are allowed.

- 3. The following is an examiner's statement of reasons for allowance:
 - Claim 1, 10, 12 are allowed because the prior art of record does not teach or suggest, in combination with the other claim limitations, a semiconductor device that includes a contact window passing through lower and upper dielectric layers, with conductive patterns that are spaced apart from the contact window disposed between the lower and upper dielectric layers, wherein a bottommost width of the contact window is wider than a distance separating two of the conductive patterns; and
 - Claims 4, 6-9 are allowed because the prior art of record does not teach or suggest, in combination with the other claim limitations, a semiconductor device that includes a contact window passing through a dielectric layer formed on a substrate, with a plurality of conductive patterns that are spaced apart from the contact window in the dielectric layer, wherein an upper region of the contact window includes a sidewall that is substantially perpendicular to the substrate and a spacer on the sidewall, and wherein a lower region of the contact window has a wider width than the upper region.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

- 4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Terauchi et al. teach a semiconductor structure that features a contact hole formed between two wirings with a lower portion of the contact hole being wider than the distance separating the two wirings.
- 5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Stephen W. Smoot whose telephone number is 571-272-1698. The examiner can normally be reached on M-F (8:00 am to 4:30 pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl Whitehead, Jr. can be reached on 571-272-1702. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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